

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (original): A method for producing a device having a reduced memory capacity comprising:

providing a device having a memory capacity;

determining that the memory capacity of the device can be reduced;

determining an amount by which the memory capacity of the device is to be reduced; and

reducing the memory capacity of the device in accordance with the determined amount to produce a device having a reduced memory capacity and a dormant memory capacity.

Claim 2 (original): The method of Claim 1 additionally comprising determining a second memory capacity of a second device in an apparatus requiring the second device having the second memory capacity.

Claim 3 (original): The method of Claim 2 wherein said apparatus comprises a computer.

Claim 4 (original): The method of Claim 2 wherein said second memory capacity of said second device is less than the memory capacity of the device.

Claim 5 (original): The method of Claim 3 wherein said second memory capacity of said second device is less than the memory capacity of the device.

Claim 6 (original): The method of Claim 2 additionally comprising furnishing said apparatus with said device having said reduced memory capacity and the dormant memory capacity which is not available for use.

Claim 7 (original): The method of Claim 3 additionally comprising furnishing said apparatus with said device having said reduced memory capacity and the dormant memory capacity which is not available for use.

Claim 8 (original): The method of Claim 4 additionally comprising furnishing said apparatus with said device having said reduced memory capacity and the dormant memory capacity which is not available for use.

Claim 9 (original): The method of Claim 5 additionally comprising furnishing said apparatus with said device having said reduced memory capacity and the dormant memory capacity which is not available for use.

Claim 10 (original): The method of Claim 2 wherein the difference between the memory capacity of the device and the second memory capacity of the second device comprises a dormant memory capacity.

Claim 11 (original): The method of Claim 3 wherein the difference between the memory capacity of the device and the second memory capacity of the second device comprises a dormant memory capacity.

Claim 12 (original): The method of Claim 1 wherein said device comprises a module.

Claim 13 (original): The method of Claim 3 wherein said device comprises a module.

Claim 14 (original): The method of Claim 2 wherein said second device comprises a second module.

Claim 15 (original): The method of Claim 3 wherein said second device comprises a second module.

Claims 16-28 (previously cancelled)

Claim 29 (original): A device produced in accordance with the method of Claim 1.

Claim 30 (previously cancelled)

Claim 31 (currently amended): An assembly comprising an apparatus; and a device disposed in said apparatus and having a reduced memory capacity and a dormant memory capacity, wherein a memory capacity of the device is reduced in accordance with a determined amount in order to achieve the reduced memory capacity.

Claim 32 (original): The assembly of Claim 31 wherein said device having been produced in accordance with the following steps:

providing the device having a memory capacity;

determining that the memory capacity of the device can be reduced;

determining an amount by which the memory capacity of the device is to be reduced; and

reducing the memory capacity of the device in accordance with the determined amount to produce the device having the reduced memory capacity and the dormant memory capacity.

Claim 33 (currently amended): A computer assembly comprising a computer; and a dual inline memory module (DIMM) disposed in said computer and having a reduced memory capacity and a dormant memory capacity, wherein a memory capacity of the DIMM is reduced in accordance with a determined amount in order to achieve the reduced memory capacity.

Claim 34 (original): The computer assembly of Claim 33 wherein said DIMM having been produced in accordance with the following steps:

providing the DIMM having a memory capacity;

determining that the memory capacity of the DIMM can be reduced;

determining an amount by which the memory capacity of the DIMM is to be reduced; and

reducing the memory capacity of the DIMM in accordance with the determined amount to produce the DIMM having the reduced memory capacity and the dormant memory capacity.

Claim 35 (original): The method of Claim 1 additionally comprising testing the produced device for a reduced memory-capacity accuracy and for a dormant memory-capacity accuracy.

Claim 36 (currently amended): A method for producing a device having a reduced memory capacity, the method comprising:

providing a device having a memory capacity;
determining an amount of reduction for the memory capacity of the device; and
reducing the memory capacity of the device by ~~an~~ the amount of reduction in order to produce a device having a reduced memory capacity and a dormant memory capacity.

Claim 37 (previously presented): The method of claim 36, wherein the device comprises a DIMM.

Claim 38 (previously presented): The method of claim 36, wherein reducing the memory capacity of the device comprises:

making at least one row of a memory bank in the device as dormant.

Claim 39 (previously presented): The method of claim 36, wherein reducing the memory capacity of the device comprises:

making at least one column of a memory bank in the device as dormant.

Claim 40 (previously presented): The method of claim 36, wherein reducing the memory capacity of the device comprises:

making at least one memory bank of the device as dormant.

Claim 41 (previously presented): The method of claim 36, further comprising:

testing the device for memory-reduction accuracy to ensure that a desired amount of memory capacity was reduced in the device.

Claim 42 (previously presented): A device produced in accordance with the method of claim 36.

Claim 43 (currently amended): An apparatus comprising:

a device having a reduced memory capacity and a dormant memory capacity, wherein a memory capacity of the device is reduced in accordance with a determined amount in order to achieve the reduced memory capacity.

Claim 44 (previously presented): The apparatus of claim 43, wherein the device comprises a DIMM.

Claim 45 (previously presented): The apparatus of claim 43, wherein the device is produced in accordance with a method comprising:

providing the device having a memory capacity; and
reducing the memory capacity of the device by an amount of reduction in order to produce a device having a reduced memory capacity and a dormant memory capacity.

Claim 46 (previously presented): The apparatus of claim 45, wherein reducing the memory capacity of the device comprises:

making at least one row of a memory bank in the device as dormant.

Claim 47 (previously presented): The apparatus of claim 45, wherein reducing the memory capacity of the device comprises:

making at least one column of a memory bank in the device as dormant.

Claim 48 (previously presented): The apparatus of claim 45, wherein reducing the memory capacity of the device comprises:

making at least one memory bank of the device as dormant.

Claim 49 (previously presented): The apparatus of claim 45, wherein the method further comprises:

testing the device for memory-reduction accuracy to ensure that a desired amount of memory capacity was reduced in the device.

Claim 50 (new): The assembly of claim 31, wherein a determination is made if the memory capacity of the device can be reduced prior to determination of the determined amount.

Claim 51 (new): The assembly of claim 33, wherein a determination is made if the memory capacity of the DIMM can be reduced prior to determination of the determined amount.

Claim 52 (new): The method of claim 36, further comprising:

prior to determining the amount of reduction, determining if the memory capacity of the device can be reduced.

Claim 53 (new): The apparatus of claim 43, wherein a determination is made if the memory capacity of the device can be reduced.

Claim 54 (new): An assembly comprising an apparatus; and a device disposed in said apparatus and having a reduced memory capacity and a dormant memory capacity;

wherein said device having been produced in accordance with the following steps:

providing the device having a memory capacity;

determining that the memory capacity of the device can be reduced;

determining an amount by which the memory capacity of the device is to be reduced; and

reducing the memory capacity of the device in accordance with the determined amount to produce the device having the reduced memory capacity and the dormant memory capacity.

Claim 55 (new): A computer assembly comprising a computer; and a dual inline memory module (DIMM) disposed in said computer and having a reduced memory capacity and a dormant memory capacity;

wherein said DIMM having been produced in accordance with the following steps:

providing the DIMM having a memory capacity;

determining that the memory capacity of the DIMM can be reduced;

determining an amount by which the memory capacity of the DIMM is to be reduced; and

reducing the memory capacity of the DIMM in accordance with the determined amount to produce the DIMM having the reduced memory capacity and the dormant memory capacity.